| Ref # | Hits | Search Query | DBs | Default Operator | Plurals | Time Stamp |
|---|---|---|---|---|---|---|
| L1 | 1158 | (711/118).CCLS. | USPAT; USOCR | OR | OFF | 2005/01/07 19:27 |
| L2 | 345 | (711/119).CCLS. | USPAT; USOCR | OR | OFF | 2005/01/07 19:27 |
| L3 | 512 | (711/122).CCLS. | USPAT; USOCR | OR | OFF | 2005/01/07 19:28 |
| L4 | 222 | (711/125).CCLS. | USPAT; USOCR | OR | OFF | 2005/01/07 19:28 |
| L5 | 364 | (712/210).CCLS. | USPAT; USOCR | OR | OFF | 2005/01/07 19:28 |
| L6 | 135 | (712/211).CCLS. | USPAT; USOCR | OR | OFF | 2005/01/07 19:28 |
| L7 | 218 | (712/213).CCLS. | USPAT; USOCR | OR | OFF | 2005/01/07 19:28 |
| L8 | 513 | (713/324).CCLS. | USPAT; USOCR | OR | OFF | 2005/01/07 19:28 |
| L9 | 147 | (micro-op$1 or micro-operation$1 or uop$1 or (micro adj1 op$1) or (micro adj1 operation$1) or micro$op or micro$ops or micro$operation or micro$operations or trace or decoded) adj1 cache$1 | USPAT | OR | OFF | 2005/01/07 19:28 |
| L10 | 607 | (713/320).CCLS. | USPAT; USOCR | OR | OFF | 2005/01/07 19:28 |
| L11 | 851 | (power$3 or suppl$3 or voltage$1) near2 (hit$1 or miss$2 or mishit$1) | USPAT | OR | OFF | 2005/01/07 19:29 |
| L12 | 174 | (micro-op$1 or micro-operation$1 or uop$1 or (micro adj1 op$1) or (micro adj1 operation$1) or micro$op or micro$ops or micro$operation or micro$operations or trace or decoded) adj1 cache$1 | US-PGPUB; EPO; JPO; DERWENT; IBM_TDB | OR | OFF | 2005/01/07 19:29 |
| L13 | 783 | (power$3 or suppl$3 or voltage$1) near2 (hit$1 or miss$2 or mishit$1) | US-PGPUB; EPO; JPO; DERWENT; IBM_TDB | OR | OFF | 2005/01/07 19:29 |
| L14 | 0 | ((micro-op$1 or micro-operation$1 or uop$1 or (micro adj1 op$1) or (micro adj1 operation$1) or micro$op or micro$ops or micro$operation or micro$operations or trace or decoded) adj1 cache$1) and ((power$3 or suppl$3 or voltage$1) near2 (hit$1 or miss$2 or mishit$1)) | US-PGPUB; EPO; JPO; DERWENT; IBM_TDB | OR | OFF | 2005/01/07 19:29 |

| L15 | 2 | ((micro-op$1 or micro-operation$1 or uop$1 or (micro adj1 op$1) or (micro adj1 operation$1) or micro$op or micro$ops or micro$operation or micro$operations or trace or decoded) adj1 cache$1) and ((power$3 or suppl$3 or voltage$1) near2 (hit$1 or miss$2 or mishit$1)) | USPAT | OR | OFF | 2005/01/07 19:30 |
|-----|----|----|-------|----|-----|-----------------|
| L16 | 31 | (((711/118).CCLS.) or ((711/119).CCLS.) or ((711/125).CCLS.) or ((711/122).CCLS.) or ((712/210).CCLS.) or ((712/211).CCLS.) or ((712/213).CCLS.) or ((713/324).CCLS.) or ((713/320).CCLS.)) and ((micro-op$1 or micro-operation$1 or uop$1 or (micro adj1 op$1) or (micro adj1 operation$1) or micro$op or micro$ops or micro$operation or micro$operations or trace or decoded) adj1 cache$1) | USPAT | OR | OFF | 2005/01/07 19:30 |
| L17 | 54 | (((711/118).CCLS.) or ((711/119).CCLS.) or ((711/125).CCLS.) or ((711/122).CCLS.) or ((712/210).CCLS.) or ((712/211).CCLS.) or ((712/213).CCLS.) or ((713/324).CCLS.) or ((713/320).CCLS.)) and ((power$3 or suppl$3 or voltage$1) near2 (hit$1 or miss$2 or mishit$1)) | USPAT | OR | OFF | 2005/01/07 19:34 |
| L18 | 47 | (((711/118).CCLS.) or ((711/119).CCLS.) or ((711/125).CCLS.) or ((712/210).CCLS.) or ((712/211).CCLS.) or ((712/213).CCLS.) or ((713/324).CCLS.) or ((713/320).CCLS.)) and ((power$3 or suppl$3 or voltage$1) near2 (hit$1 or miss$2 or mishit$1)) | USPAT | OR | OFF | 2005/01/07 19:31 |
| L19 | 29 | (((711/118).CCLS.) or ((711/119).CCLS.) or ((711/125).CCLS.) or ((712/210).CCLS.) or ((712/211).CCLS.) or ((712/213).CCLS.) or ((713/324).CCLS.) or ((713/320).CCLS.)) and ((micro-op$1 or micro-operation$1 or uop$1 or (micro adj1 op$1) or (micro adj1 operation$1) or micro$op or micro$ops or micro$operation or micro$operations or trace or decoded) adj1 cache$1) | USPAT | OR | OFF | 2005/01/07 19:31 |

| L20 | 2 | (((((711/118).CCLS.) or ((711/119).CCLS.) or ((711/125).CCLS.) or ((711/122).CCLS.) or ((712/210).CCLS.) or ((712/211).CCLS.) or ((712/213).CCLS.) or ((713/324).CCLS.) or ((713/320).CCLS.)) and ((micro-op$1 or micro-operation$1 or uop$1 or (micro adj1 op$1) or (micro adj1 operation$1) or micro$op or micro$ops or micro$operation or micro$operations or trace or decoded) adj1 cache$1)) not (((((711/118).CCLS.) or ((711/119).CCLS.) or ((711/125).CCLS.) or ((712/210).CCLS.) or ((712/211).CCLS.) or ((712/213).CCLS.) or ((713/324).CCLS.) or ((713/320).CCLS.)) and ((micro-op$1 or micro-operation$1 or uop$1 or (micro adj1 op$1) or (micro adj1 operation$1) or micro$op or micro$ops or micro$operation or micro$operations or trace or decoded) adj1 cache$1) ) | USPAT | OR | OFF | 2005/01/07 19:31 |
| L21 | 7 | (((((711/118).CCLS.) or ((711/119).CCLS.) or ((711/125).CCLS.) or ((711/122).CCLS.) or ((712/210).CCLS.) or ((712/211).CCLS.) or ((712/213).CCLS.) or ((713/324).CCLS.) or ((713/320).CCLS.)) and ((power$3 or suppl$3 or voltage$1) near2 (hit$1 or miss$2 or mishit$1))) not (((((711/118).CCLS.) or ((711/119).CCLS.) or ((711/125).CCLS.) or ((712/210).CCLS.) or ((712/211).CCLS.) or ((712/213).CCLS.) or ((713/324).CCLS.) or ((713/320).CCLS.)) and ((power$3 or suppl$3 or voltage$1) near2 (hit$1 or miss$2 or mishit$1)) ) | USPAT | OR | OFF | 2005/01/07 19:31 |